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DEC 16 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant: Shawn L. Lloyd et al. §  
Serial No.: 10/822,572 §  
Filed: April 12, 2004 §  
For: Making Electrical Connections §  
Between a Circuit Board and §  
an Integrated Circuit §

Art Unit: 2833  
Examiner: James R. Harvey  
Docket: ITL.1113US  
P18785  
Assignee: Intel Corporation

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed November 7, 2005, reconsideration is respectfully requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: December 16, 2005  
I hereby certify that this document is being facsimile transmitted to the  
United States Patent and Trademark Office (Fax No. 571/273-8300) on  
the date indicated above.  
*Cynthia L. Hayden*  
Cynthia L. Hayden